Optimal power allocation and load balancing for non-dedicated heterogeneous distributed embedded computing systems

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HIGHLIGHTS

- The considered system is fully heterogeneous.
- Task types and priorities are diverse.
- Two new rules are observed from a great deal of data experiments.
- An optimal power allocation and load balancing strategy is proposed.

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ABSTRACT

This paper investigates on the optimal power allocation and load balancing problem encountered by heterogeneous and distributed embedded systems with mixed tasks. Given that each node has real and different urgent tasks in the majority of practical heterogeneous embedded systems, three priority disciplines are considered: dedicated jobs without priority, prioritized dedicated jobs without preemption, and prioritized dedicated jobs with preemption. A model is established for heterogeneous embedded processors with dedicated-task-dependent dynamic power and load balancing management; each processor is considered as an M/M/1 queueing sub-model with mixed generic and dedicated tasks. The processors have different levels of power consumption, and each one can employ any of the three disciplines. The objective of this study is to find an optimal load balancing (for generic tasks) and power allocation strategy for heterogeneous processors preloaded by different amounts of dedicated tasks such that the average response time of generic tasks is minimized. Considering that this problem is a multi-constrained, multi-variable optimization problem for which a closed-form solution is unlikely to be obtained, we propose an optimal power allocation and load balancing scheme by employing Lagrange method and binary search approach, which are completed by utilizing two new rules established by observing numerical variations of parameters. Several numerical examples are presented to demonstrate the effectiveness of our solution. To the best of our knowledge, this is the first work on analytical study that combines load balancing, energy efficiency, and priority of tasks in heterogeneous and distributed embedded systems.

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1. Introduction

1.1. Motivation

In view of the increasing computing demands and performance requirements of modern applications, complex embedded platforms are now equipped with multiple computing nodes, in which each node owns its dedicated jobs. These nodes vary in computing speed and power consumption. As such, embedded platforms exhibit characteristics of heterogeneous computing, embedded computing, and distributed computing. Typical
examples of embedded platforms are the BMW 7 Series cars, in which over 100 electronic control units (ECUs) are embedded in a single car with dedicated tasks for each ECU.

Computing performance is a vital metric when a system's quality of service (QoS) is being evaluated. Higher computing performance results in a shorter average waiting time for tasks and a higher system throughput. High computing performance depends on the effective utilization of computing resources. Therefore, assigning generic tasks to nodes without affecting their own dedicated tasks is an effective strategy for performance improvement of embedded systems. In distributed systems, task allocation assigned by a load-balancing algorithm is directly related to the overall performance of the entire system. Thus, an efficient load-balancing strategy is vital to building a distributed-computing architecture, particularly an embedded distributed system with limited resource.

Energy efficiency is important in most computing environments. Embedded systems depend even more heavily on energy efficiency and optimization than general systems. Energy efficiency is related to the performance and runtime of a system, especially for battery-powered devices. Energy management can be achieved by using dynamic power management (DPM) [19] or dynamic voltage scaling (DVS) [32] to regulate power consumption. DPM implemented at the operating-system level provides the supply voltage and clock-frequency adjustment schemes while tasks are running. This strategy offers opportunities for tuning the energy-delay tradeoff [27].

In distributed embedded systems, each node owns its dedicated tasks, which may be urgent tasks for the system. Thus, in such environments, the priority of dedicated tasks should be considered when tasks are being assigned to a node. The urgency of the dedicated tasks of each node is not the same. Thus, the priority strategy could be set in various ways. That is, some dedicated tasks can be considered concomitant general tasks, some tasks possess a higher priority than general tasks, and some tasks possess a higher priority with pre-emption.

With the development of embedded computing, heterogeneous and multi-node architectures have become a trend. In heterogeneous distributed embedded environments with limited computing resource and power, a system should be capable of operating with optimal load balancing and power allocation without impacting the system’s important tasks.

1.2. Our contributions

In this paper, we aim to develop power- and performance-constrained load-distribution methods for current and future embedded architectures. We examine the problem of assigning a set of general tasks to computing nodes of a computational heterogeneous embedded distributed system, wherein each node is pre-loaded with different amounts of dedicated tasks and is equipped with a DVS feature. However, each dedicated task for each node has a different priority because of the differences in urgency to the system. According to the urgency of the dedicated tasks, three types of priority disciplines of nodes in the system are as follows:

- Discipline 1: All general tasks and dedicated tasks on this node are scheduled with a first-come, first-served discipline without priority.
- Discipline 2: Dedicated tasks are always scheduled before general tasks on this node. All tasks are executed without interruption.
- Discipline 3: Dedicated tasks are always scheduled before general tasks on this node and with pre-emption.

We refer to Discipline 1 as dedicated tasks without priority, Discipline 2 as prioritized dedicated tasks without pre-emption, and Discipline 3 as prioritized dedicated tasks with pre-emption. Each node may employ any of the three disciplines. We treated each embedded computing node as an M/M/1 queueing model with infinite waiting-queue capacity.

The contributions of this study are as follows:

- An algorithm is proposed for finding the optimal load-distribution and power-allocation schemes of the system, such that the overall average response time for generic tasks is minimized.
- The proposed algorithm is highly adaptable, such that it can be used to deal with heterogeneous nodes with different amounts of pre-loaded dedicated tasks, different queueing disciplines, as well as different maximum speeds and power consumption.
- We provide several numerical examples to demonstrate the effectiveness of our algorithm from different perspectives. We also plot the functions of overall response time of generic tasks, node utilization of each node, power allocation on each node, and task assignment on each node of the arrival rate of generic tasks. Through these functions, we can observe the changing tendency of these parameters as the arrival rate of generic tasks changes.

Our study is focused on a well-defined, multi-constrained, and multi-variable problem. To the best of our knowledge, no study has investigated an optimization problem that considers the combination of load distribution, energy efficiency, and task priority in heterogeneous embedded distributed systems. Our investigation makes a significant contribution to high-performance and energy-efficient computing in modern heterogeneous and distributed embedded systems.

2. Related work

Load distribution and balancing in general distributed and parallel computing systems have been extensively studied, and a huge body of literature exists [3,15,25,29]. The majority of existing works on optimal load distribution have focused on performance metrics and system characteristics. Performance metrics include mean response time [7], arithmetic average response time [13], average consensus [34], mean response ratio [28], mean miss ration [9], and probability of load-balancing success [21]. Distributed-system characteristics refer to two main considerations: (1) whether the server is heterogeneous, and (2) whether each server has its own dedicated jobs and whether these jobs have higher priority. In Ref. [36], the authors studied how to quickly find the minimum response time of an application running on a heterogeneous system. In Ref. [35], the adaptive dynamic scheduling on heterogeneous embedded systems was investigated. In Ref. [6], the system contains non-priority dedicated jobs and generic jobs, and the objective of this paper is to seek assignment probabilities of generic jobs such that the average response time of a job is minimized. In Ref. [22], the author considered a heterogeneous environment that includes general jobs and multiple types of non-priority dedicated jobs; this work explored the problem of optimal job-dispatching probabilities for minimum average job response time. In Ref. [12], three types of priorities, namely dedicated jobs without priority, priority dedicated jobs without pre-emption, and priority dedicated jobs with pre-emption were considered in a heterogeneous environment; however, in Ref. [12], all of the priority disciplines of each node are set to be the same and not heterogeneous. In Ref. [18], the quantitative modeling and analytical calculation of elasticity in distributed systems was studied. In Ref. [17], optimal task
dispatching on multiple heterogeneous multi-server systems was studied. Virtually, the nature of problems discussed in Refs. [18] and [17] are still load balancing.

Energy efficiency is necessary in both distributed (e.g., cloud computing, grid computing) or embedded environments. For large-scale distributed systems, high energy consumption leads to severe economic, environmental, and ecological problems. For embedded systems, high energy consumption will affect the system’s runtime, particularly that of battery-power devices. The intuitionistic policy for reducing power consumption is to efficiently manage system power. In Ref. [11], the author investigated the green-energy-aware power-management problem for distributed-computing data centers. In Ref. [8], the author summarized and classified resource-management problems in distributed-computing environments. In Ref. [20], the author summarized the approaches and techniques proposed in recent works in this discipline. The purpose of energy efficiency is to make power consumption proportional to system utilization [4]. Energy efficiency can be achieved by applying many different approaches, such as algorithmic, stochastic, and learning-based approaches [1]. In Ref. [26], the author developed a methodology for cycle-accurate simulation of performance and energy consumption in embedded systems. In Ref. [39], a framework was built for designing and analyzing the energy cost of algorithms. Basic software techniques for energy efficiency are supported by a mechanism called dynamic frequency scaling (equivalently, DVS, dynamic speed scaling, and dynamic power scaling). DPM at the operating-system level provides supply-voltage and clock-frequency adjustment schemes implemented while tasks are running. A huge body of literature is available on this topic. In Ref. [5], the author summarized several classes of power-managed systems and power-management strategies. These energy-saving techniques explore the opportunities for tuning the energy-delay tradeoff. In Ref. [23], the researcher specifically studied the problem of system-level design techniques for the energy efficiency of embedded systems.

Both performance and energy efficiency are important for most systems, especially for distributed embedded systems. In general, three policies can be implemented to combine DPM and performance to achieve energy efficiency. The first is to optimize the performance by fixing the energy consumption constraint. In Ref. [7], the author addressed the problem of minimizing average response time of tasks under a given power. In Ref. [14], the author minimized the average task response time of multiple heterogeneous servers under the condition that the overall power consumption of all servers does not exceed a pre-set power limit. In Ref. [16], the author studied the problem of finding the optimal speed scheme of servers with workload-dependent DPM to minimize the average task response time under a given power. The second consideration is to minimize the power consumption under a performance constraint [30, 31, 37, 39]. The author of Ref. [30] proposed a method that seeks the minimum energy consumption of a specific task set by using resource sharing that meets the time constraint. In Ref. [31], the author used dynamic voltage-frequency scaling to minimize power consumption for precedence-constrained parallel task execution without increasing the entire task’s execution time. The third consideration is to optimize both power consumption and performance [10, 33]. In Ref. [10], the energy consumption and the make span are minimized without violating the deadlines and the tasks’ architectural requirements. In Ref. [33], the author minimized the weighted sum of mean response time and power consumption while scheduling processor sharing.

3. Models and problem formulation

3.1. Power model

The power dissipation of CMOS circuits mainly consists of three parts, namely, dynamic, static, and short-circuits consumption, among which dynamic power consumption is the dominant component. The dynamic power consumption can be expressed by $P = kCV^2f$ where $k$ is an activity factor, $C$ is the loading capacitance, $V$ is the supply voltage, and $f$ is the clock frequency. The speed $s$ of a core is often defined as the number of instructions the core can perform per second (IPS). Given that $s \propto f$ and $f \propto V$, then $P \propto s^2$, where $\alpha$ is around 3 [38]. For ease of discussion, we model the dynamic power allocated to node $v_i$ with speed $s_i$, as $s^\alpha_i$. Thus, we formulate the power consumption of an embedded computing node as $P_i = s^\alpha_i + P_i^\sigma$, where $P_i^\sigma$ is the static power consumption independent of the clock rate.

3.2. Queueing model

We establish a model to formulate and study the problem of power allocation and load balancing in a heterogeneous distributed embedded environment. We assume that we have $n$ heterogeneous embedded computing nodes $v_1, v_2, \ldots, v_n$ (simply called as a node), each of which has its own dedicated set of jobs, which is a Poisson stream of tasks with arrival rate $\lambda_i$ that can only be executed on it. There exists a general Poisson stream of tasks with arrival rate $\lambda$ that needs to be executed by being split into $n$ sub-streams $\lambda_i$ assigned to each node. Thus, each node deals with a combined stream of dedicated and general tasks. The structure of the system is showed in Fig. 1. The task execution requirements of dedicated and general tasks are exponential random variables with mean $\overline{T}_i$ and $\overline{T}_R$, respectively. Thus, the two types of mean execution times on node $v_i$ are $\overline{T}_i = \overline{T}_R / \lambda_i$, $\overline{T}_R = \overline{T}_R / \lambda$, respectively. Since both the arrival rate and processing rate of tasks submit to Poisson distribution, each node can be treated as an $M/M/1$ queueing system. Parameters used are shown in Table 1. To maintain the queue steady, we assume that $\rho_i < 1$, for all $1 \leq i \leq n$.

3.3. Problem formulation

Our problem can be specified as follows: given $n$ numbers of embedded nodes/cores $v_1, v_2, \ldots, v_n$, the arrival rates $\lambda_1, \lambda_2, \ldots, \lambda_n$. 

<p>| Table 1 |</p>
<table>
<thead>
<tr>
<th>Notations</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>$s_i$</td>
<td>The speed of node $v_i$</td>
</tr>
<tr>
<td>$\lambda_i$</td>
<td>Arrival rate of dedicated tasks to $v_i$</td>
</tr>
<tr>
<td>$\lambda_i^*$</td>
<td>Arrival rate of general tasks to $v_i$</td>
</tr>
<tr>
<td>$\lambda_i =$</td>
<td>$\lambda_i + \lambda_i^*$</td>
</tr>
<tr>
<td>$\lambda =$</td>
<td>$\sum \lambda_i$</td>
</tr>
<tr>
<td>$\overline{\lambda}_i$</td>
<td>Average execution time of dedicated tasks on $v_i$</td>
</tr>
<tr>
<td>$\overline{\lambda}_i^*$</td>
<td>Average execution time of general tasks on $v_i$</td>
</tr>
<tr>
<td>$\overline{\lambda}_i$</td>
<td>Average service rate of dedicated tasks on $v_i$</td>
</tr>
<tr>
<td>$\overline{\lambda}_i^*$</td>
<td>Average service rate of general tasks on $v_i$</td>
</tr>
<tr>
<td>$\overline{\lambda}_i^2$</td>
<td>The second moment of dedicated tasks on $v_i$</td>
</tr>
<tr>
<td>$\overline{\lambda}_i^2$</td>
<td>The second moment of general tasks on $v_i$</td>
</tr>
<tr>
<td>$m_i$</td>
<td>$= (\overline{\lambda}_i / \lambda_i) \cdot \overline{\lambda}_i + (\overline{\lambda}_i / \lambda_i) \cdot \overline{\lambda}_i$</td>
</tr>
<tr>
<td>$\overline{\lambda}_i$</td>
<td>$\overline{\lambda}_i = \overline{\lambda}_i / \lambda_i$</td>
</tr>
<tr>
<td>$\overline{\lambda}_i^* = \overline{\lambda}_i^* / \lambda_i$</td>
<td></td>
</tr>
<tr>
<td>$\rho_i$</td>
<td>Average percentage of time that node $v_i$ is busy</td>
</tr>
<tr>
<td>$T_i$</td>
<td>Average response time of general tasks on $v_i$</td>
</tr>
<tr>
<td>$\overline{T}_i$</td>
<td>Average response time of general tasks on system</td>
</tr>
</tbody>
</table>
\( \tilde{\lambda}_n \) of dedicated tasks on the \( n \) nodes, the average tasks execution requirements \( \tilde{T}_1, \tilde{T}_2, \ldots, \tilde{T}_n \) of dedicated tasks on the \( n \) nodes, the total arrival rate \( \lambda \) of general tasks, the average task execution requirements \( \tilde{\tau} \) of general tasks, the base power supply \( P_1^*, P_2^*, \ldots, P_n^* \), the available power \( \tilde{P} \), and the queuing discipline of each node, find the tasks arrival rates \( \hat{\lambda}_1, \hat{\lambda}_2, \ldots, \hat{\lambda}_n \) and the core speeds \( s_1, s_2, \ldots, s_n \) such that the average response time of general tasks on the system is minimized, namely,

\[
T = \frac{\hat{\lambda}_1}{\tilde{\lambda}} T_1 + \frac{\hat{\lambda}_2}{\tilde{\lambda}} T_2 + \cdots + \frac{\hat{\lambda}_n}{\tilde{\lambda}} T_n,
\]

subject to

\[
\hat{\lambda}_1 + \hat{\lambda}_2 + \cdots + \hat{\lambda}_n = \hat{\lambda},
\]

and

\[
\tilde{\tau} = \sum_{i=1}^{n} P_i = \sum_{i=1}^{n} (s_i^m + P_i^*),
\]

where \( T_i \) (\( 1 \leq i \leq n \)) represents the average response time of generic tasks on \( v_i \).

4. The proposed method

4.1. Basic derivation

Each node is treated as an M/M/1 queuing system. For different queuing disciplines, the average response time of general tasks \( T_i \) is different. We split all nodes into three groups according to the queuing discipline. Group \( G_1 \) includes all of the nodes whose priority disciplines are Discipline 1, group \( G_2 \) includes all of the nodes whose priority disciplines are Discipline 2, and group \( G_3 \) includes all of the nodes whose priority disciplines are Discipline 3. If \( v_i \in G_1 \), we have \([2], \text{p. 702}\)

\[
T_i = \tilde{\lambda}_i + \frac{\lambda_i m_{i}}{2 (1 - \rho_i)}
\]

\[
= \tilde{\tau} + \frac{\tilde{\lambda}^2_i + \tilde{\tau}^2_i}{s_i (s_i - \tilde{\lambda}_i - \tilde{\tau}_i)}.
\]

If \( v_i \in G_2 \), we have \([2], \text{p. 704}\)

\[
T_i = \frac{1}{1 - \rho_i} \left( \tilde{\lambda}_i + \frac{\lambda_i m_{i}}{2 (1 - \rho_i)} \right)
\]

\[
= \frac{1}{s_i - \tilde{\lambda}_i} \left( \tilde{\tau} + \frac{\tilde{\lambda}^2_i + \tilde{\tau}^2_i}{s_i - \tilde{\lambda}_i - \tilde{\tau}_i} \right).
\]

Our target is to minimize Eq. (1). Note that \( \hat{\lambda} \) in Eq. (1) is a fixed value, it could be ignored that would not impact the original solution. To solve Eq. (1), we use the Lagrange multiplier system, and set the following functions:

\[
\psi(s_1, s_2, \ldots, s_n) = P - \sum_{i=1}^{n} P_i,
\]

and

\[
\varphi(\tilde{\lambda}_1, \tilde{\lambda}_2, \ldots, \tilde{\lambda}_n) = \sum_{i=1}^{n} \tilde{\lambda}_i - \hat{\lambda}.
\]

as two Lagrange constraint functions, and construct a Lagrange function:

\[
L = \sum_{i=1}^{n} \hat{\lambda}_i T_i + \phi \psi(s_1, s_2, \ldots, s_n) + \tau \varphi(\hat{\lambda}_1, \hat{\lambda}_2, \ldots, \hat{\lambda}_n),
\]

where \( \phi \) and \( \tau \) are two Lagrange multipliers.

Eq. (7) includes \( 2n + 2 \) variables, which are \( \hat{\lambda}_1, \hat{\lambda}_2, \ldots, \hat{\lambda}_n, s_1, s_2, \ldots, s_n, \phi, \tau \). We take the partial derivative with respect to
\(\hat{\lambda}_i\), which is
\[
\frac{\partial (\hat{\lambda}_i T_i)}{\partial \lambda_i} = \phi \frac{\partial \psi}{\partial \lambda_i} + r \frac{\partial \psi}{\partial \lambda_i}.
\]
(8)

According to Eqs. (1) and (8), we have
\[
T_i + \frac{\partial T_i}{\partial \lambda_i} = \phi.
\]
(9)

In Eq. (9), \(T_i\) and \(\frac{\partial T_i}{\partial \lambda_i}\) vary under different priority disciplines. Hence, \(\hat{\lambda}_i\) should be solved according to the priority discipline of \(v_i\).

For \(v_i \in G_1\), based on Eqs. (4) and (9), we have
\[
T_i + \frac{\partial T_i}{\partial \lambda_i} = \phi.
\]
(10)

Eq. (10) can be transformed into Eq. (11),
\[
\hat{\lambda}_i = -b_i - 2\sqrt{b_i^2 - 4a_i c_i}.
\]
(12)

where
\[
t_i = \frac{1}{r} \left\{ \frac{t_i (\pi t_i + \lambda_i r_i^2)}{s_i \phi} \right\},
\]
and is unlikely to get a closed-form solution. So we have to

For \(v_i \in G_2\), based on Eqs. (5) and (9), we have
\[
\frac{\partial (\hat{\lambda}_i T_i)}{\partial \lambda_i} = \phi \frac{\partial \psi}{\partial \lambda_i} + r \frac{\partial \psi}{\partial \lambda_i}.
\]
(13)

Eq. (13) can be transformed into Eq. (14),
\[
\hat{\lambda}_i = \frac{1}{r} \left\{ \frac{t_i (\pi t_i + \lambda_i r_i^2)}{s_i \phi} \right\},
\]
and
\[
t_i = \frac{1}{r} \left\{ \frac{t_i (\pi t_i + \lambda_i r_i^2)}{s_i \phi} \right\},
\]

where
\[
\frac{\partial \psi}{\partial \lambda_i} = -b_i - 2\sqrt{b_i^2 - 4a_i c_i},
\]
and
\[
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\]
and
\[
t_i = \frac{1}{r} \left\{ \frac{t_i (\pi t_i + \lambda_i r_i^2)}{s_i \phi} \right\},
\]

where
\[
\frac{\partial \psi}{\partial \lambda_i} = -b_i - 2\sqrt{b_i^2 - 4a_i c_i},
\]
and
\[
\frac{\partial \psi}{\partial \lambda_i} = -b_i - 2\sqrt{b_i^2 - 4a_i c_i}.
\]
Observing Eq. (24) we can find that if the value of \( \phi \) is fixed, the function \( g(\lambda, s_i) \) includes only one variable \( s_i \). This observation is very helpful. Although \( s_i \) for different nodes may be different, \( \tau \) for all nodes is the same because it is a Lagrange multiplier. If \( \tau \) could determine \( s_i \), then \( \tau \) could also determine \( s_j \) where \( 0 \leq i, j \leq n \). In other words, \( \tau \) may be able to determine \( s_i \) for all \( 0 \leq i \leq n \).

In order to analyze the relationship between \( \tau \) and \( s_i \), we first need to determine the monotonicity of \( \tau \) with respect to \( s_i \). \( \tau = g(\lambda, s_i) \) is a high power and nonlinear function of \( s_i \), which is difficult to analyze the monotonicity based on mathematical derivation. We assign 0.0001, 0.0002, \ldots, 5 to \( s_i \) respectively, and then obtain the corresponding \( g(f(\phi, 0.0001), 0.0001), g(f(\phi, 0.0002), 0.0002), \ldots, g(f(\phi, 5), 5) \). By drawing the points (0.0001, \( g(f(\phi, 0.0001), 0.0001) \)), \( g(f(\phi, 0.0002), 0.0002) \), \ldots, (5, \( g(f(\phi, 5), 5) \)) on the coordinate axis, the monotonicity can be analyzed based on the graph of \( g(\lambda, s_i) \).

Fig. 2 shows graphs \( g(\lambda, s_i) \) of four nodes all belonging to \( C_3 \), and each node is preloaded with different amounts of tasks, where \( \phi \) is set to 7. Meanwhile, the nodes shown in Figs. 3 and 4 are the same as that shown in Fig. 2 expect that \( \phi \) is set to 5 and 3, respectively. From Figs. 2 to 4, we summarize a rule and describe it as Rule 1. In fact, in order to verify this rule, many related experiments have been tested, and we can obtain the same result. Owing to limited space, it is impossible to list all of these experiments.

**Rule 1:** With each fixed value of \( \phi \), the value of function \( \tau = g(\lambda, s_i) \) initially increased with an increase in \( s_i \) and then decreased as \( s_i \) continued to increase, for all \( 1 \leq i \leq n \).

In Figs. 2 to 4, \( \tilde{s}_i \) represents the corresponding value of \( s_i \) when \( g(\lambda, s_i) \) is maximum, namely \( g(\lambda_1, s_i) \leq g(\lambda_2, s_i) \). Obviously, each \( g(\lambda, s_i) \) is a convex function when \( s_i \) is located at \( [\tilde{s}_i, +\infty) \). On the basis of convex optimization theory, \( \lambda_1, \lambda_2, \ldots, \lambda_n \) will be the optimal solution, if \( s_i \) is located at \( [\tilde{s}_i, +\infty) \) for all \( 1 \leq i \leq n \) could be met simultaneously. To obtain the optimal solution, two steps are designed. First, given a value of \( \phi \), find a \( \tau \) that can be used to determine the speeds \( s_1, s_2, \ldots, s_n \) under the power constraint Eq. (3). Then, based on the first step, by adjusting the value of \( \phi \), find the appropriate \( \phi \) and \( \tau \) that simultaneously meet the constraints equation (3) and equation (2). The following two paragraphs describe the two steps.

This paragraph explains step one. For \( g(\lambda, s_i) \), \( (\tilde{s}_i, g(\lambda, \tilde{s}_i)) \) is a turning point, which makes \( g(\lambda, s_i) \) obtain the maximum value, i.e., \( g(\lambda_1, s_i) \leq g(\lambda_2, s_i) \). The Lagrange multiplier \( \tau \) should have the same value for all \( g(\lambda, s_i) \). Thus, the upper bound of \( \tau \) is the smallest maximum value of all \( g(\lambda, s_i) \), namely \( \tau \leq \min \{ g(\lambda_1, \tilde{s}_1), g(\lambda_2, \tilde{s}_2), \ldots, g(\lambda_n, \tilde{s}_n) \} \). At the right side of \( \tilde{s}_i \) under the power constraint, \( g(\lambda, s_i) \) is monotonically decreasing. In other words, with a given \( \tau \), the corresponding \( s_i \) can be immediately obtained by using binary search. Power \( P_i \) is a monotonically increasing function of \( s_i \) for all \( 1 \leq i \leq n \). Since \( \tau \) could determine \( s_1, s_2, \ldots, s_n \), \( s_i \in [\tilde{s}_i, +\infty) \), we can obtain all new \( s_i \) satisfying Eq. (3).
meet Eq. (3), the rangeability of $s_i$ remains relatively stable even if changing $\phi$ will change the value of $s_i$. This finding suggests that the value of $\phi$ is the dominant factor affecting the value of $\lambda_i$. On this basis, we introduce the second observed rule.

**Rule 2:** On the premise of all $s_i$ locating at $[\bar{s}_i, +\infty]$ and $s_1, s_2, \ldots, s_n$ satisfying Eq. (3), $\lambda_i$ will increase monotonically with $\phi$, indicating that $\sum_{i=1}^{n} \lambda_i$ will increase monotonically with $\phi$.

According to above analyses and Rules 1 and 2, we can design algorithms to find the optimal solution $\lambda_1, \lambda_2, \ldots, \lambda_n, s_1, s_2, \ldots, s_n$, and corresponding Lagrange multipliers $\phi$ and $\tau$. These algorithms will be introduced in next section.

### 4.3. Algorithm design

In this section, we will design algorithms to find the optimal solution $\lambda_1, \lambda_2, \ldots, \lambda_n, s_1, s_2, \ldots, s_n$, and the corresponding Lagrange multipliers $\phi$ and $\tau$. Based on Rules 1 and 2 mentioned in Section 4.2, our method is divided to four steps for solving problems, which are described as follows:

1. Solve the searching region for $s_i$.
2. Find the turning point $\bar{s}_i$ on the searching region of $s_i$.
3. Fix the value of $\phi$, and find a $\tau$ on region $[\bar{s}_i, +\infty]$ which can make $s_1, s_2, \ldots, s_n$ satisfy Eq. (3).
4. Based on steps 1, 2 and 3, adjust $\phi$ until both Eqs. (2) and (3) are satisfied.

The searching region for $s_i$ is determined by $\lambda_i = f_i(\phi, s_i) > 0$, and could be solved based on Eq. (19).

For $v_i \in G_1$, we can get

$$s_i - \bar{s}_i \geq \frac{1}{\bar{\tau}} \sqrt{\left(\lambda_i \bar{\tau}_i \phi + \bar{\tau} \right) \left(\lambda_i \bar{\tau}_i i + \bar{\tau} \right)}.$$

Then, we will have

$$a_i s_i^2 + b_i s_i + c_i \geq 0,$$

where

$$a_i = \phi, b_i = \left(\lambda_i \bar{\tau}_i \phi + \bar{\tau} \right), c_i = \bar{\tau} \left(\bar{\tau} - \bar{\tau}_i \right).$$

We set that

$$\Delta_i = b_i^2 - 4a_i c_i = \left(\lambda_i \bar{\tau}_i \phi + \bar{\tau} \right)^2 - 4\lambda_i \bar{\tau}_i \left(\bar{\tau} - \bar{\tau}_i \right).$$

As we know, in real situations of distributed environments, we always expect that a node whose CPU speed is improved could be assigned with more tasks, We called it as “more energy more tasks”, which indicates that $\lambda_i$ increases with the increase of $s_i$. Based on “more energy more tasks”, we define the searching region for $s_i \ (v_i \in G_1)$ as follows:

$$s_i \geq \frac{\left(\lambda_i \bar{\tau}_i \phi + \bar{\tau} \right) \sqrt{2\phi}}{1}, \quad \Delta_i \leq 0;$$

$$s_i \geq \frac{1}{2\phi} \left(\sqrt{\left(\lambda_i \bar{\tau}_i \phi + \bar{\tau} \right)^2 - 4\lambda_i \bar{\tau}_i \left(\bar{\tau} - \bar{\tau}_i \right)} \right) + \lambda_i \bar{\tau}_i \phi + \bar{\tau}, \quad \Delta_i > 0. \quad (25)$$

Similarly, for $v_i \in G_2$, we can obtain

$$a_i s_i^3 + b_i s_i^2 + c_i s_i + d_i \geq 0,$$

where

$$a_i = \phi, b_i = \left(2\bar{\phi} \lambda_i \bar{\tau}_i \phi \right).$$

$$c_i = \lambda_i \bar{\tau}_i \left(2\bar{\tau} + \lambda_i \bar{\tau}_i \phi \right),$$

$$d_i = -\left(\lambda_i \bar{\tau}_i \phi \right)^2 \bar{\tau}.$$

Eq. (26) is a cubic polynomial equation about $s_i$. To solve it we need to use Fan’s formula [24]. Let

$$A_i = b_i^2 - 3a_i c_i, B_i = b_i c_i - 9a_i d_i, C_i = c_i^2 - 3b_i d_i.$$

Let $\Delta_i = B_i^2 - 4A_i C_i$ as the discriminant. Taking the value of $\Delta_i$ and the “more energy more tasks” into consideration, we obtain the searching region for $s_i \ (v_i \in G_2)$ as follows:

$$s_i \geq \frac{-b_i + B_i}{3a_i} \frac{\tau}{\bar{\tau}} \Delta_i = 0; \quad \frac{-b_i + \sqrt{A_i} \cos \frac{\phi}{2} + \sqrt{3} \sin \frac{\phi}{2}}{3a_i} \Delta_i < 0.$$

Therefore, $\phi(s_i - \bar{s}_i)^2 - \tau(s_i - \bar{s}_i \bar{\tau}_i) - \lambda_i \bar{\tau}_i \geq 0$. \quad (28)

From Eq. (28) we obtain the searching region for $s_i \ (v_i \in G_3)$, which is

$$s_i \geq \frac{\tau + \sqrt{\tau^2 + 4\phi \lambda_i \bar{\tau}_i^2}}{2\phi} + \lambda_i \bar{\tau}_i, \quad \Delta_i < 0.$$

We have solved the searching region for $s_i \ (0 \leq i \leq n)$. The next step is to find the turning point $\bar{s}_i$ on the searching region of $s_i$

From Figs. 2 to 4, we observe that $g_i(\lambda_i, s_i)$ is an increasing function when $s_i$ locates at the left side of $\bar{s}_i$, and $g_i(\lambda_i, s_i)$ is a decreasing function when $s_i$ locates at the right side of $\bar{s}_i$. This means that the derivative of $g_i(\lambda_i, s_i)$ is larger than 0, $g_i'(\lambda_i, s_i) > 0$, if $s_i < \bar{s}_i$, and $g_i'(\lambda_i, s_i) < 0$ if $s_i > \bar{s}_i$. Therefore, $\bar{s}_i$ can be solved using binary search based on $g_i'(\lambda_i, s_i)$. Due to the attention only on $s_i$, for the sake of simplicity, we replace the partial derivative of $f_i(\phi, s_i)$ with $f_i'(s_i)$, and replace the partial derivative of $g_i(\lambda_i, s_i)$ with $g_i'(\lambda_i, s_i)$.

$$g_i'(\lambda_i, s_i) = \frac{\partial^2 \bar{\tau}_i}{\partial s_i^2} f_i(s_i) - \left(\frac{\left(\alpha_i - 1\right)f_i(s_i) - f_i'(s_i)}{s_i} \right) \frac{\partial \bar{\tau}_i}{\partial s_i} \quad (30)$$

where

$$f_i'(s_i) = \frac{1}{\bar{\tau} \sqrt{\phi \lambda_i \bar{\tau}_i^3}} \left(\frac{1}{\phi \lambda_i \bar{\tau}_i^3 + \bar{\phi} \lambda_i \bar{\tau}_i} \right)^{3/2} 2\sqrt{\bar{\lambda}_i \bar{\tau}_i^2}, \quad v_i \in G_2;$$

$$f_i'(s_i) = \frac{1}{\bar{\tau} \sqrt{\phi \lambda_i \bar{\tau}_i^3}} \left(\frac{1}{\phi \lambda_i \bar{\tau}_i^3 + \bar{\phi} \lambda_i \bar{\tau}_i} \right)^{3/2} 2\sqrt{\bar{\lambda}_i \bar{\tau}_i^2}, \quad v_i \in G_2;$$

$$f_i'(s_i) = \frac{1}{\bar{\tau} \sqrt{\phi \lambda_i \bar{\tau}_i^3}} \left(\frac{1}{\phi \lambda_i \bar{\tau}_i^3 + \bar{\phi} \lambda_i \bar{\tau}_i} \right)^{3/2} 2\sqrt{\bar{\lambda}_i \bar{\tau}_i^2}, \quad v_i \in G_3;$$

$$f_i'(s_i) = \frac{1}{\bar{\tau} \sqrt{\phi \lambda_i \bar{\tau}_i^3}} \left(\frac{1}{\phi \lambda_i \bar{\tau}_i^3 + \bar{\phi} \lambda_i \bar{\tau}_i} \right)^{3/2} 2\sqrt{\bar{\lambda}_i \bar{\tau}_i^2}, \quad v_i \in G_3;$$

$$f_i'(s_i) = \frac{1}{\bar{\tau} \sqrt{\phi \lambda_i \bar{\tau}_i^3}} \left(\frac{1}{\phi \lambda_i \bar{\tau}_i^3 + \bar{\phi} \lambda_i \bar{\tau}_i} \right)^{3/2} 2\sqrt{\bar{\lambda}_i \bar{\tau}_i^2}, \quad v_i \in G_3;$$
\[ d_i = \tilde{r}_i + \lambda_i \tilde{r}^2_i, \quad t_i = s_i - \lambda_i \tilde{r}_i. \]

For \( v_i \in G_1 \), we have
\[
\frac{\partial^2 T_i}{\partial s_i^2} = -\frac{2T_i}{t_i^3} + \frac{2t_i - \lambda_i \tilde{r}}{t_i^4(t_i - \lambda_i \tilde{r})^2} \times \left( \lambda_i \tilde{r}^2 - (\lambda_i \tilde{r}^2 + \lambda_i \tilde{r}^2) \right) \times \left( \frac{1}{t_i} + \frac{1 - \lambda_i \tilde{r}}{t_i - \lambda_i \tilde{r}} \right)
\]
\[
- \frac{1}{t_i} \left( \lambda_i \tilde{r}^2 + \lambda_i \tilde{r}^2 \right) \times \left( \frac{1}{t_i} + \frac{1 - \lambda_i \tilde{r}}{t_i - \lambda_i \tilde{r}} \right).
\]

For \( v_i \in G_2 \), we have
\[
\frac{\partial^2 T_i}{\partial s_i^2} = \frac{\hat{r}}{\lambda_i s_i^2} \left( f'_i(s_i) - \frac{2}{s_i} \right) + \frac{H_i}{\lambda_i t_i(t_i - \lambda_i \tilde{r})^2},
\]
where
\[
H_i = f'_i(s_i) \left( 2 - \frac{\lambda_i \tilde{r}}{t_i} \right) \times (2\lambda_i \tilde{r}^2 + \lambda_i \tilde{r}^2 - \lambda_i \tilde{r}_i + \lambda_i \tilde{r}^2) \times \left( 6t_i + \lambda_i \tilde{r} \right)\left( \frac{2\lambda_i \tilde{r}}{t_i} - 6 \right) + \frac{2f'_i(s_i)(\lambda_i \tilde{r} - 3t_i)}{t_i \left( t_i - \lambda_i \tilde{r} \right)}. \]

For \( v_i \in G_3 \), we have
\[
\frac{\partial^2 T_i}{\partial s_i^2} = -\frac{2T_i}{t_i^3} + \frac{2t_i - \lambda_i \tilde{r}}{t_i^4(t_i - \lambda_i \tilde{r})^2} \times \left( f'_i(s_i)^2 - (\lambda_i \tilde{r}^2 + \lambda_i \tilde{r}^2) \right) \times \left( \frac{1}{t_i} + \frac{1 - f'_i(s_i)}{t_i - \lambda_i \tilde{r}} \right)
\]
\[
- \frac{1}{t_i} \left( \lambda_i \tilde{r}^2 + \lambda_i \tilde{r}^2 \right) \times \left( \frac{1}{t_i} + \frac{1 - \lambda_i \tilde{r}}{t_i - \lambda_i \tilde{r}} \right).
\]

The binary search will be used many times. In order to avoid repeatedly using a list of search methods, we define it in Algorithm 1. According to this definition, \( \tilde{s} \) could be solved by biSearch(low, max, \( g_i(\lambda_i, s_i) > 0 \)), where low is supported by Eqs. (25), (27), and (29); max can be an appropriate value, for instance, it could be set to 5 because at present most processor’s maximal frequency is less than 5 GHz.

**Algorithm 1** biSearch(lb, ub, criterion)

**Require:** lb, ub, criterion.
**Ensure:** var.

1. while (ub - lb > \( \varepsilon \)) do
2. var \( \leftarrow (lb + ub)/2; \)
3. if criterion then
4. lb \( \leftarrow \) var;
5. else
6. ub \( \leftarrow \) var;
7. end if
8. end while
9. return var.

In this paragraph, we will introduce how to find a \( \tau \) on region \([\tilde{s}, +\infty]\) which can make \( s_1, s_2, \ldots, s_n \) satisfy Eq. (3). \( \tau \) is a common Lagrange multiplier. Since \( \tau \) is considered as \( \tau = g(\lambda_i, s_i) \), the upper bound of \( \tau \) is the smallest maximum value of all \( g(\lambda_i, s_i) \) (Algorithm 2). On region \([\tilde{s}, +\infty]\), \( g(\lambda_i, s_i) \) is a decreasing function of \( s_i \). In other words, given a \( \tau \), the corresponding \( s_i \) can be solved by binary search, i.e., biSearch(\( \tilde{s}, max, g(\lambda_i, s_i) > \tau \)). Since \( \tau \) is a monotonous function of \( s_i \), \( \tau \) is also a monotonous function of \( \sum_{i=1}^n (s_i^\alpha + P_i^\alpha) \). Algorithm 3 shows how to find a \( \tau \) which can make \( s_1, s_2, \ldots, s_n \) satisfy \( \sum_{i=1}^n (s_i^\alpha + P_i^\alpha) \leq \overline{P} \). In Algorithm 3, the \( s_i \) shown in line 7 is calculated by biSearch(\( \tilde{s}, max, g(\lambda_i, s_i) > \tau \)).

**Algorithm 2** findUBof_\( \tau \)(\( \phi, \tilde{s}_1, \ldots, \tilde{s}_n \))

**Require:** \( \phi, \tilde{s}_1, \ldots, \tilde{s}_n \).
**Ensure:** ubOf_\( \tau \).

1. ubOf_\( \tau \) \( \leftarrow \) \( y' \);
2. for \( (1 \leftarrow i; i \leq n; i \leftarrow i + 1) \) do
3. temp \( \leftarrow g_i(f_i(\tilde{s}_i, \phi)) \);
4. if temp < \( \gamma \) then
5. ubOf_\( \tau \) \( \leftarrow \) temp;
6. end if
7. end for
8. return ubOf_\( \tau \).

**Algorithm 3** calculateAllu

**Require:** \( \phi, \overline{P} \).
**Ensure:** \( s_1, s_2, \ldots, s_n \).

1. \( \tilde{s}_1, \tilde{s}_2, \ldots, \tilde{s}_n \);
2. for \( (1 \leftarrow i; i \leq n; i \leftarrow i + 1) \) do
3. \( \tilde{s}_i \leftarrow\) biSearch(low, max, \( g_i(\lambda_i, s_i) > 0 \)));
4. end for
5. ub \( \leftarrow \) findUBof_\( \tau \)(\( \phi, \tilde{s}_1, \tilde{s}_2, \ldots, \tilde{s}_n \))
6. lb \( \leftarrow 0; \)
7. \( \tau \leftarrow\) biSearch(lb, ub, \( \sum_{i=1}^n (s_i^\alpha + P_i^\alpha) < \overline{P} \));
8. return \( \tau, s_1, s_2, \ldots, s_n \).

Based on the Rule 2 mentioned in Section 4.2, we can use binary search to obtain the appropriate \( \phi \), which can then be used to calculate \( s_1, s_2, \ldots, s_n \) and \( \lambda_1, \lambda_2, \ldots, \lambda_n \) that meet Eqs. (2) and (3) at the same time.

**Algorithm 4** calculateLBof_\( \phi \)

**Require:** \( \lambda_1, \lambda_2, \ldots, \lambda_n, s_1, s_2, \ldots, s_n, \overline{r}_1, \overline{r}_2, \ldots, \overline{r}_n, \overline{P} \).
**Ensure:** lBof_\( \phi \).

1. ubOf_\( \phi \) \( \leftarrow 1.0; \)
2. repeat
3. \( \phi \leftarrow 2\phi; \)
4. for \( (1 \leftarrow i; i \leq n; i \leftarrow i + 1) \) do
5. \( \tilde{s}_i \leftarrow\) biSearch(low, max, \( g_i(\lambda_i, s_i) > 0 \)));
6. end for
7. \( \tau \leftarrow\) findUBof_\( \tau \)(\( \phi, \tilde{s}_1, \tilde{s}_2, \ldots, \tilde{s}_n \));
8. for \( (1 \leftarrow i; i \leq n; i \leftarrow i + 1) \) do
9. \( s_i \leftarrow\) biSearch(\( \tilde{s}_i, max, g_i(\lambda_i, s_i) > \tau \));
10. end for
11. until \( \sum_{i=1}^n (s_i^\alpha + P_i^\alpha) \geq \overline{P} \);
12. lb \( \leftarrow 0, ub \leftarrow \phi; \)
13. \( \phi \leftarrow\) biSearch(lb, ub, \( \sum_{i=1}^n (s_i^\alpha + P_i^\alpha) \geq \overline{P} \));
14. //The \( s_i \) in line 13 is calculated by
15. \( \tilde{s}_i \leftarrow\) biSearch(low, max, \( g_i(\lambda_i, s_i) > 0 \));
16. \( \tau \leftarrow\) findUBof_\( \tau \)(\( \phi, \tilde{s}_1, \tilde{s}_2, \ldots, \tilde{s}_n \));
17. \( s_i \leftarrow\) biSearch(\( \tilde{s}_i, max, g_i(\lambda_i, s_i) > \tau \));
18. return \( s_1, \ldots, s_n, \phi_B, \lambda_B \leftarrow \sum_{i=1}^n f_i (s_i, \phi) \).
As mentioned, the major steps of our method can be summed up as: given a φ, search a τ that can be used to find s₁, s₂, ..., sₙ satisfying Eq. (3); and then adjust φ until both Eqs. (2) and (3) are satisfied. We know that \( \sum_{i=1}^{n} f_i(\phi, s_i) \) decreases monotonically when φ decreases, therefore a small value of \( \lambda \) will be matched with a small φ. However, Figs. 2 to 4 suggest that when φ is small, there may not exist a τ that makes all s_i not only locate at the right side of \( \bar{s} \) but also satisfy Eq. (3), i.e., \( \bar{p} = \sum_{i=1}^{n} (s_{mi} + P_i^*) \). For instance, from Figs. 2 to 4, \( g_i(\phi, \bar{s}, \bar{r}) \) is about 28, 15 and 5 when \( \phi = 7 \) and 3, respectively. When φ becomes smaller, then \( \lambda \) will also become smaller, causing \( \bar{s} \) to become larger. If φ is too small, then \( \sum_{i=1}^{n} (s_{mi} + P_i^*) \leq \bar{p} \) cannot be satisfied.

To deal with the situation with a small \( \lambda \), we will find a threshold φ. When \( \phi \geq \phi_0 \), there exists a τ that makes s₁, s₂, ..., sₙ not only locate at the right side of \( \bar{s} \) but also satisfy Eq. (3). When \( \phi < \phi_0 \), such a τ does not exist. According to whether τ exists, \( \phi_0 \) can be solved by binary search. Assume that we get \( \sum_{i=1}^{n} f_i(\phi, s_i) = \lambda_B \) and s₁, s₂, ..., sₙ = s₁B, s₂B, ..., sₙB satisfying \( \sum_{i=1}^{n} (s_{mi} + P_i^*) = \bar{p} \), when \( \phi = \phi_0 \). When \( \lambda < \lambda_B \), we still adopt s₁B, s₂B, ..., sₙB, that is, keep the speeds of nodes unchanged. Based on s₁B, s₂B, ..., sₙB and \( \sum_{i=1}^{n} \hat{\lambda}_i = \lambda \), the load balancing \( \hat{\lambda}_1, \hat{\lambda}_2, \ldots, \hat{\lambda}_n \) can be solved by binary search.

Notice that the \( \phi_0 \) and \( \hat{\lambda}_0 \) are independent of \( \lambda \). For a system under analysis, we first calculate the threshold \( \phi_0 \) and \( \hat{\lambda}_0 \). Algorithm 4 shows how \( \phi_0 \) can be obtained. Algorithm 5 shows how the final \( \hat{\lambda}_1, \hat{\lambda}_2, \ldots, \hat{\lambda}_n, s_1, s_2, \ldots, s_n, \phi \) and τ can be determined.

Algorithm 5 calculate_T

Require: \( \hat{\lambda}_1, \hat{\lambda}_2, \ldots, \hat{\lambda}_n, s_1, s_2, \ldots, s_n, \bar{r}, \bar{t}, \bar{p}, \bar{\lambda} \).
Ensure: \( \lambda_1, \lambda_2, \ldots, \lambda_n, s_1, s_2, \ldots, s_n, \phi, \tau \).
1:  \( s_1, s_2, \ldots, s_n \leftarrow \text{calculateAll}(\hat{\lambda}) \);
2:  if \( (\lambda > \hat{\lambda}_B) \) then
3:  \( \bar{lb} \leftarrow \phi_0 \);
4:  repeat
5:  \( \phi \leftarrow 2\phi \);
6:  \( s_1, s_2, \ldots, s_n \leftarrow \text{calculateAll}(\phi) \);
7:  until \( \hat{\lambda}_1 + \hat{\lambda}_2 + \cdots + \hat{\lambda}_n > \lambda \);
8:  \( ub \leftarrow \phi \);
9:  \( \phi \leftarrow \text{biSearch}(\bar{lb}, \bar{ub}, n, f_i(s_i) < \lambda) \); //The speeds
10:  /\( s_1, s_2, \ldots, s_n \) in line 9 are calculated by Algorithm 3.
11:  else
11:  \( \bar{lb} \leftarrow 0, \bar{ub} \leftarrow \phi_0 \);
12:  \( \phi \leftarrow \text{biSearch}(\bar{lb}, \bar{ub}, n, \hat{\lambda}_i < \lambda) \); //Note that speed s_i is not
13:  /\( \text{considered here.} \)
14:  \( \text{return} \( \hat{\lambda}_1, \ldots, \hat{\lambda}_n, s_1, s_2, \ldots, s_n, \phi, \tau, T_1, T_2, \ldots, T_n \). \)

5. Numerical examples

In this section, we demonstrate a number of numerical examples. All parameters in our examples are for illustration only. They can be changed to any other real values.

Example 1. In this example, we consider the situation in which the preloaded tasks of each node are heterogeneous, whereas the priority discipline and dynamic power consumption of each node are homogeneous. We consider a group of n = 7 embedded nodes \( v_1, v_2, \ldots, v_7 \). The base power consumption is \( P_i^* = 0.1 \) W for all \( 1 \leq i \leq n \). The preloaded dedicated tasks and \( \omega_i \) of each node are shown in Table 2. The unit of task execution requirement is giga instructions, and the unit of task arrival rate is number of tasks per second. The general tasks execution requirement is \( \tau = 0.35 \) (giga instructions), and its arrival rate is \( \lambda = 16.5 \) per/second. We assume \( P = 30 \) W. In Tables 3, 4, and 5, we show the optimal load distribution \( \hat{\lambda}_1, \hat{\lambda}_2, \ldots, \hat{\lambda}_7 \), the optimal node speed \( s_1, s_2, \ldots, s_7 \), the node utilizations \( \rho_1, \rho_2, \ldots, \rho_7 \) and the average general task response time \( T_1, T_2, \ldots, T_7 \) respectively, for Discipline 1, Discipline 2 and Discipline 3. The overall average task response time of the n nodes is \( T = 0.7717698 \) s, \( T = 0.9770131 \) s and \( T = 1.0462909 \) s, respectively.

Example 2. In this example, we consider the situation in which the preloaded tasks and the priority discipline of each node are homogeneous, whereas the dynamic power consumption of each node are heterogeneous. We also consider a group of n = 7 embedded nodes. The base power consumption is \( P_i^* = 0.1 \) W for all \( 1 \leq i \leq n \). The preloaded dedicated tasks and \( \omega_i \) of each node are shown in Table 6. The arrival rate of general task is \( \lambda = 11 \) per second and its tasks execution requirement is \( \tau = 0.35 \) (giga instructions), and \( P = 40 \) W. The significance of Tables 7, 8, 9 is the same as that of Tables 3, 4, 5. The overall average task response time of the n nodes is \( T = 0.6543409 \) s (Discipline 1), \( T = 0.9065909 \) s (Discipline 2) and \( T = 1.0261497 \) s (Discipline 3).

Example 3. In this example, we consider the situation in which the preloaded tasks and dynamic power consumption are homogeneous, whereas the priority disciplines are heterogeneous. We consider a group of n = 6 embedded nodes \( v_1, v_2, \ldots, v_6 \),...
The input parameters in Example 2.

\[
\begin{array}{ccccccc}
\lambda_i & \alpha_i & T_i & v_1 & v_2 & v_3 & v_4 \\
1 & 2.0 & 2.0 & 2.0 & 2.0 & 2.0 & 2.0 \\
2 & 0.3 & 0.3 & 0.3 & 0.3 & 0.3 & 0.3 \\
3 & 2.8 & 2.8 & 2.8 & 2.8 & 2.8 & 2.8 \\
\end{array}
\]

Type D1 D1 D1 D2 D2 D2

Table 6
The input parameters in Example 2.

\[
\begin{array}{ccccccc}
\lambda_i & \alpha_i & T_i & v_1 & v_2 & v_3 & v_4 \\
1 & 2.0 & 2.0 & 2.0 & 2.0 & 2.0 & 2.0 \\
2 & 0.3 & 0.3 & 0.3 & 0.3 & 0.3 & 0.3 \\
3 & 2.8 & 2.8 & 2.8 & 2.8 & 2.8 & 2.8 \\
\end{array}
\]

Type D1 D1 D1 D2 D2 D2

Table 7
Numerical data in Example 2 when system priority strategy is Discipline 1.

\[
\begin{array}{ccccccc}
i & \lambda_i & \alpha_i & T_i & v_1 & v_2 & v_3 \\
1 & 2.0 & 2.0 & 2.0 & 2.0 & 2.0 & 2.0 \\
2 & 0.3 & 0.3 & 0.3 & 0.3 & 0.3 & 0.3 \\
3 & 2.8 & 2.8 & 2.8 & 2.8 & 2.8 & 2.8 \\
\end{array}
\]

Table 8
Numerical data in Example 2 when system priority strategy is Discipline 2.

\[
\begin{array}{ccccccc}
i & \lambda_i & \alpha_i & T_i & v_1 & v_2 & v_3 \\
1 & 2.0 & 2.0 & 2.0 & 2.0 & 2.0 & 2.0 \\
2 & 0.3 & 0.3 & 0.3 & 0.3 & 0.3 & 0.3 \\
3 & 2.8 & 2.8 & 2.8 & 2.8 & 2.8 & 2.8 \\
\end{array}
\]

Table 9
Numerical data in Example 2 when system priority strategy is Discipline 3.

\[
\begin{array}{ccccccc}
i & \lambda_i & \alpha_i & T_i & v_1 & v_2 & v_3 \\
1 & 2.0 & 2.0 & 2.0 & 2.0 & 2.0 & 2.0 \\
2 & 0.3 & 0.3 & 0.3 & 0.3 & 0.3 & 0.3 \\
3 & 2.8 & 2.8 & 2.8 & 2.8 & 2.8 & 2.8 \\
\end{array}
\]

Table 10
The input parameters in Example 3.

\[
\begin{array}{ccccccc}
\lambda_i & \alpha_i & T_i & v_1 & v_2 & v_3 \\
1 & 2.0 & 2.0 & 2.0 & 2.0 & 2.0 \\
2 & 0.3 & 0.3 & 0.3 & 0.3 & 0.3 \\
3 & 2.8 & 2.8 & 2.8 & 2.8 & 2.8 \\
\end{array}
\]

Type D1 D1 D1 D2 D2 D3 D3 D3

Table 11
Numerical data in Example 3 when the \( \lambda \) is 8 per second.

\[
\begin{array}{ccccccc}
i & \lambda_i & \alpha_i & T_i & v_1 & v_2 & v_3 \\
1 & 2.0 & 2.0 & 2.0 & 2.0 & 2.0 & 2.0 \\
2 & 0.3 & 0.3 & 0.3 & 0.3 & 0.3 & 0.3 \\
3 & 2.8 & 2.8 & 2.8 & 2.8 & 2.8 & 2.8 \\
\end{array}
\]

Table 12
Numerical data in Example 3 when the \( \lambda \) is 15 per second.

\[
\begin{array}{ccccccc}
i & \lambda_i & \alpha_i & T_i & v_1 & v_2 \\
1 & 2.0 & 2.0 & 2.0 & 2.0 & 2.0 \\
2 & 0.3 & 0.3 & 0.3 & 0.3 & 0.3 \\
3 & 2.8 & 2.8 & 2.8 & 2.8 & 2.8 \\
\end{array}
\]

Table 13
The input parameters in Example 4.

\[
\begin{array}{ccccccc}
\lambda_i & \alpha_i & T_i & v_1 & v_2 \\
1 & 2.0 & 2.0 & 2.0 & 2.0 \\
2 & 0.3 & 0.3 & 0.3 & 0.3 \\
3 & 2.8 & 2.8 & 2.8 & 2.8 \\
\end{array}
\]

Table 14
Numerical data in Example 4.

\[
\begin{array}{ccccccc}
\lambda_i & \alpha_i & T_i & v_1 & v_2 \\
1 & 2.0 & 2.0 & 2.0 & 2.0 \\
2 & 0.3 & 0.3 & 0.3 & 0.3 \\
3 & 2.8 & 2.8 & 2.8 & 2.8 \\
\end{array}
\]

We set the priority discipline of node \( v_1, v_2 \) is Discipline 1 (abbreviated as D1), the priority discipline of nodes \( v_3, v_4 \) is Discipline 2 (abbreviated as D2), and the priority discipline of nodes \( v_5, v_6 \) is Discipline 3 (abbreviated as D3). We set \( P^* = 0.1 \) W for all \( 1 \leq i \leq n \). The preloaded dedicated tasks and \( \alpha_i \) of each node are shown in Table 10. The tasks execution requirement is \( T = 0.35 \) (giga instructions). We assume that we are given \( P = 35 \) W. For comparison, we set the arrival rate of general task is \( \lambda = 8 \) and \( \lambda = 15 \) per second, respectively. Tables 11 and 12 show the optimal load distribution, node speed, node utilization, and average general task response time of each node respectively for \( \lambda = 8 \) and \( \lambda = 15 \) per second. The overall average task response time of the \( n \) nodes is \( T = 0.8565089 \) s.

Example 5. In this example, we consider a small \( \lambda \) under the same situation as in Example 4. We demonstrate the same data with the same input as in Example 4 except that general task arrival rate is \( \lambda = 8 \) per second. The result is shown in Table 15. The overall average task response time of the \( n \) nodes is \( T = 0.4940509 \).
and the difference between the power allocated to each node tends to stabilize as \( \tilde{\lambda} \) increases, and the difference between the power allocated to each node becomes narrow. Fig. 8 reveals that the key factors affecting the task assignment \( \tilde{\lambda} \) are the preloaded tasks and \( \alpha_i \), not priority discipline, when \( \tilde{\lambda} \) is very large.

### 6. Conclusions

We have mentioned the necessity of heterogeneous distributed-computing architecture for the development of embedded computing, as well as the importance and significance of performance optimization and power reduction in an embedded and distributed environment. We described a queueing model for a group of complete heterogeneous computing nodes with different speeds and an energy-consumption model. We presented a power-allocation and load-balancing problem under the condition that each node employs different queueing disciplines. We constructed our solution as a multi-variable and multi-constrained problem that we addressed by utilizing the Lagrange multipliers and binary search approach, which are demonstrated with some numerical examples. Our work makes an initial contribution to optimal load balancing with a dynamic power constraint for multiple queueing systems with multiple embedded computing nodes in heterogeneous and distributed embedded systems.
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